

FIG. 2

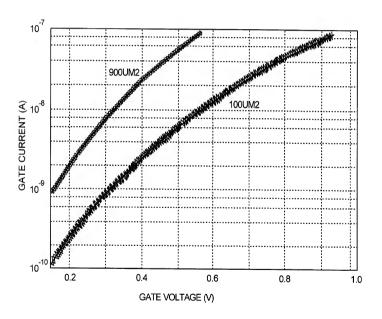


FIG. 3

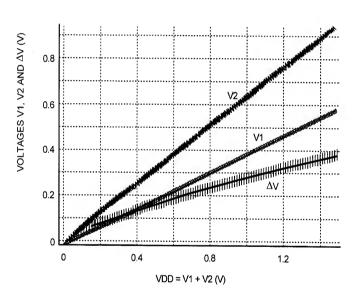
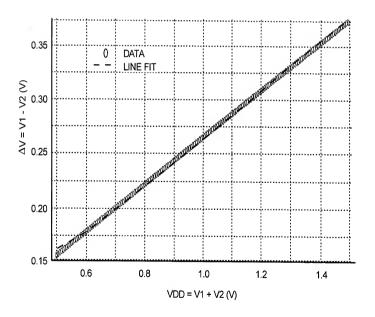


FIG. 4



*5/11* FIG. 5

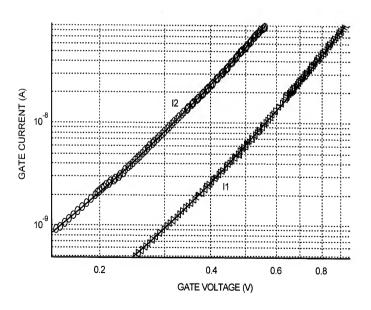


FIG. 6

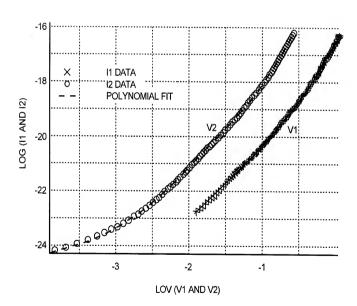


FIG. 7

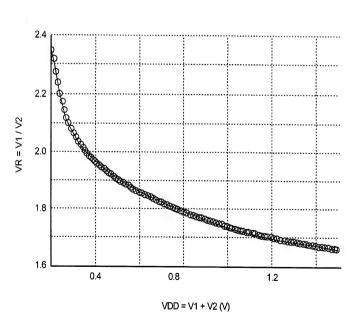


FIG. 8

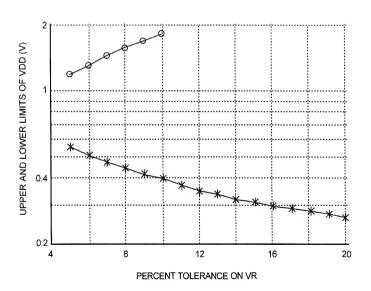
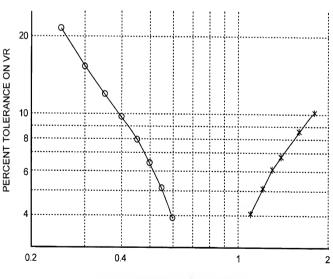


FIG. 9



LOWER AND UPPER LIMITS ON VDD (V)

10/11 FIG. 10

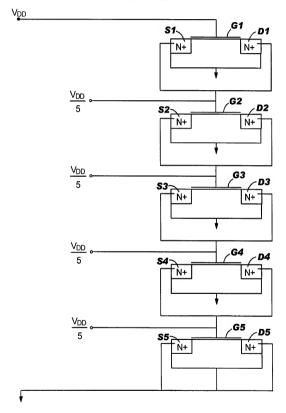


FIG. 11

